

10/029778
10/18/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10029778	FILING DATE 10/18/2001	CLASS 474	SUBCLASS 030	GAU 2841	EXAMINER ALCATA trinh
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**APPLICANTS: Doi Yutaka;

3729

**CONTINUING DATA VERIFIED:

YES (MC)

THIS APPLICATION IS A DIV OF 09/752,408 12/28/2000

ALCATA

** FOREIGN APPLICATIONS VERIFIED:

None (MC)

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no
35 USC 119 conditions met ☐ yes ☒ no
Verified and Acknowledged Examiners's initials (MC)

ATTORNEY DOCKET NO

H0001800 DIV (4960)

TITLE : Layered circuit boards and methods of production thereof

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due		Sheets Drwg.	Figs. Drwg.
Date Paid		Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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